Hanhai-tech SD NAND are highly integrated flash memories with serial and

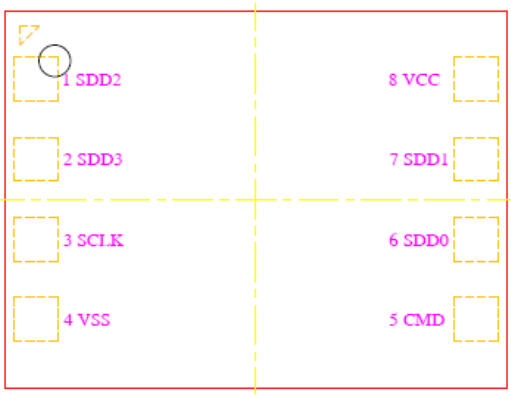


Figure 1:Top View

DAT2	I/O/PP	Data Line [Bit2]	RSV	Reserved
DAT3	I/O/PP	Data Line [Bit3]	I3	Chip select(neg true)
CLK	I	Clock	I	Clock
GND	S	Supply voltage ground	S	Supply voltage ground
VDD	S	Supply voltage	S	Supply voltage
DAT1	I/O/PP	Data Line [Bit1]	RSV	Reserved
DAT0	I/O/PP	Data Line [Bit0]	OPP	Data Out
CMD	PP	Command/Response	I	Data In

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	1GB	950MB	19.6/18 MB/S	6.20*8.00 (mm)

Standby current: 250uA Maximum value

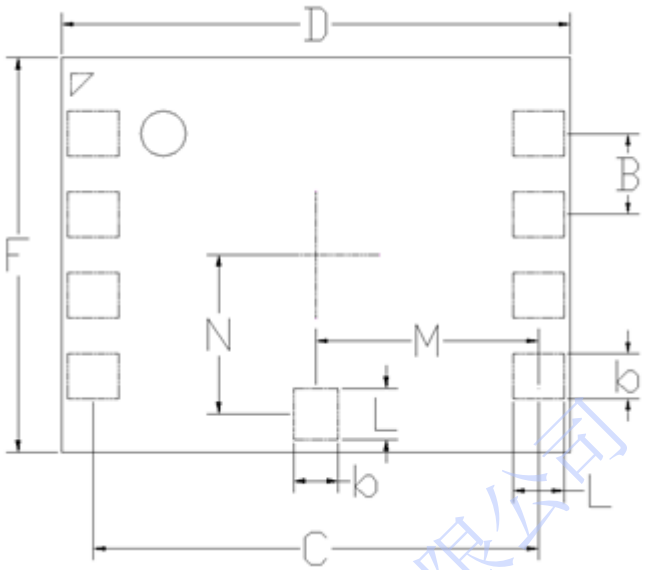
Standby current: 200uA average value

Operating current: 75mA Maximum value

Operating current: 60mA average value

Test condition: RTS5308 card reader (Voltage 3.3V), Fluke289c multi-meter.

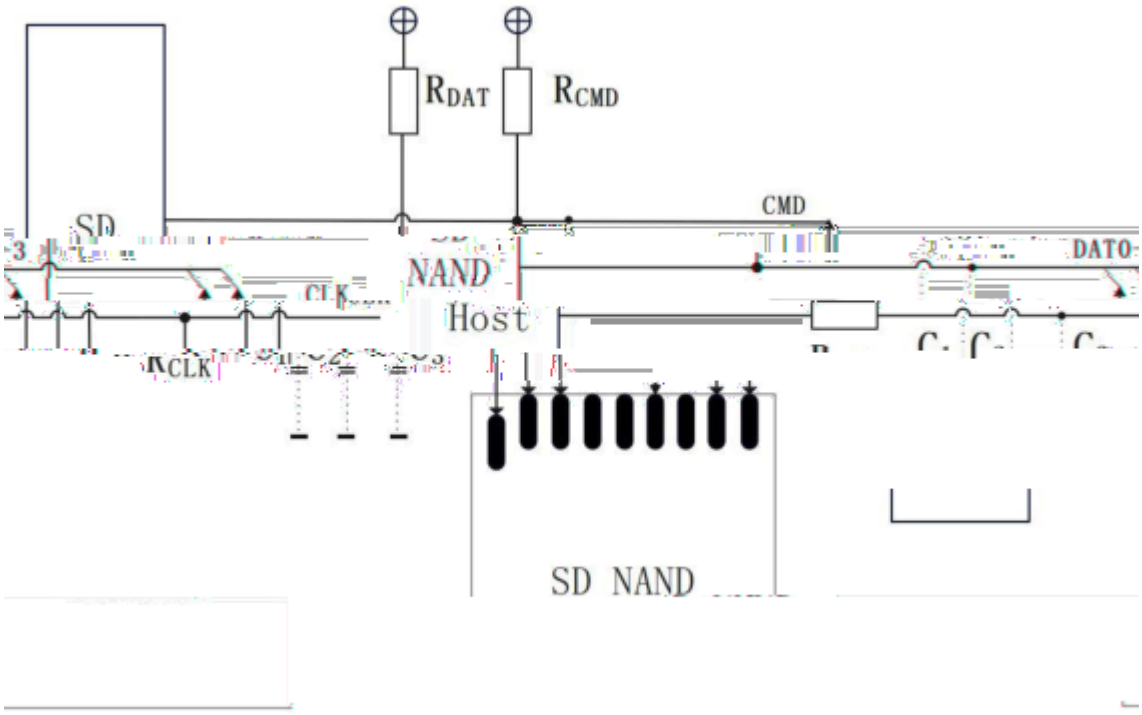
Temperature	Operating	-40 ~85
	Non-Operating	-65 ~150
Humidity	Operating	25% to 85%, non-condensing



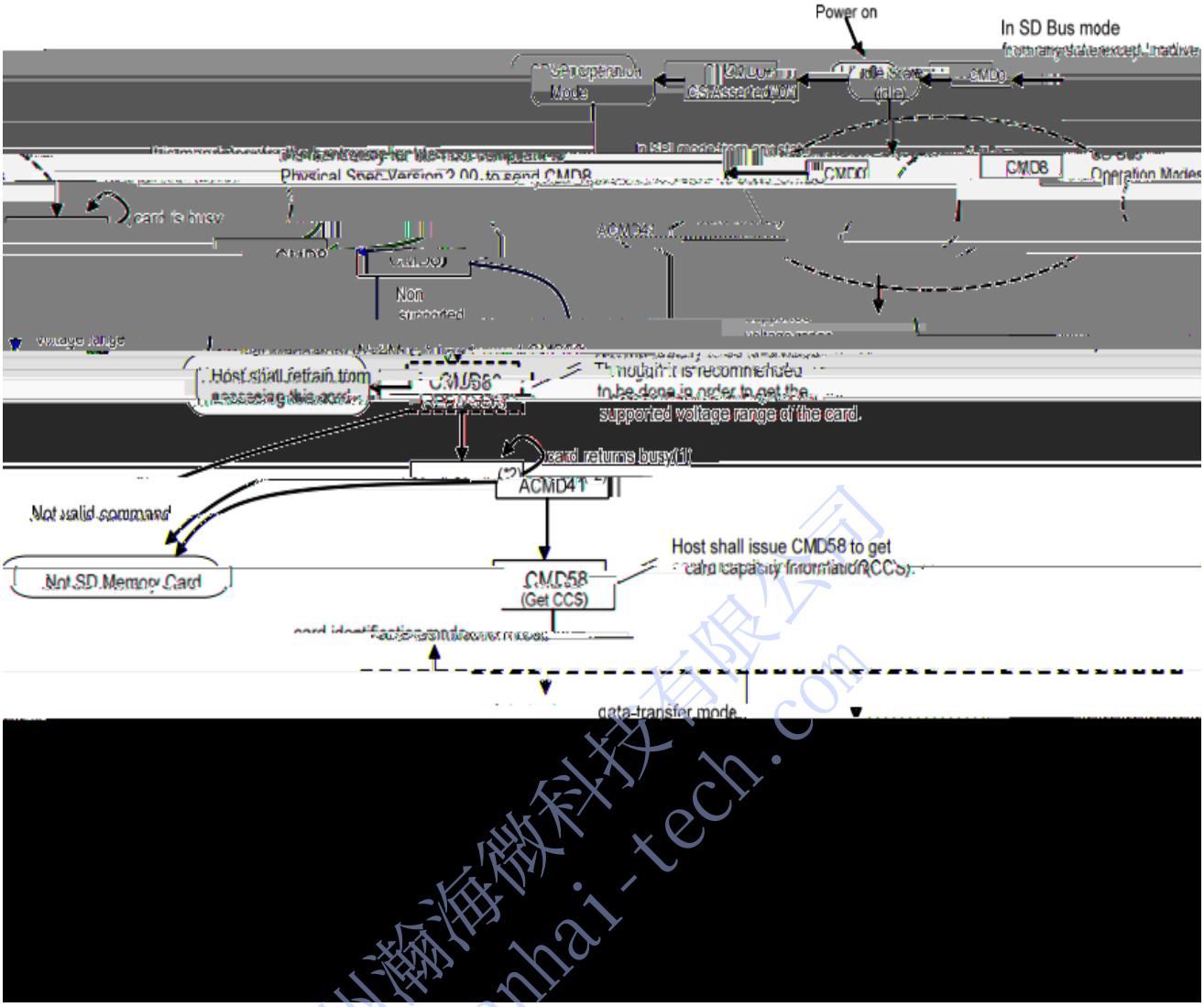
Common Dimensions			
B	-	1.27(Typ)	-
D	7.90	8.00	8.10
E	5.90	6.00	6.10
F	6.10	6.20	6.30
H	0.70	0.80	0.90

SD NAND Package Dimensions (Units: mm)

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RDAT
RCMD
CMD
DAT0
NAND
Host
CLK
SD NAND

Below table shows the typical data management of the card when the host writes RUs of an AU. When the host writes to a fragmented AU, the card prepares a new AU by copying the used RUs and writing the new RUs.. The location A is at the start of the AU boundary and location B is at the end of the AU boundary. From A to B, the host shall write data to free RUs contiguously and skip used RUs (shall not skip any free RU). The card may indicate busy to the host, so the host can wait, during the time the card controller is writing and moving data. The total write time from A to B can bnn mn ca tn a6.4 29.4suma6. ud

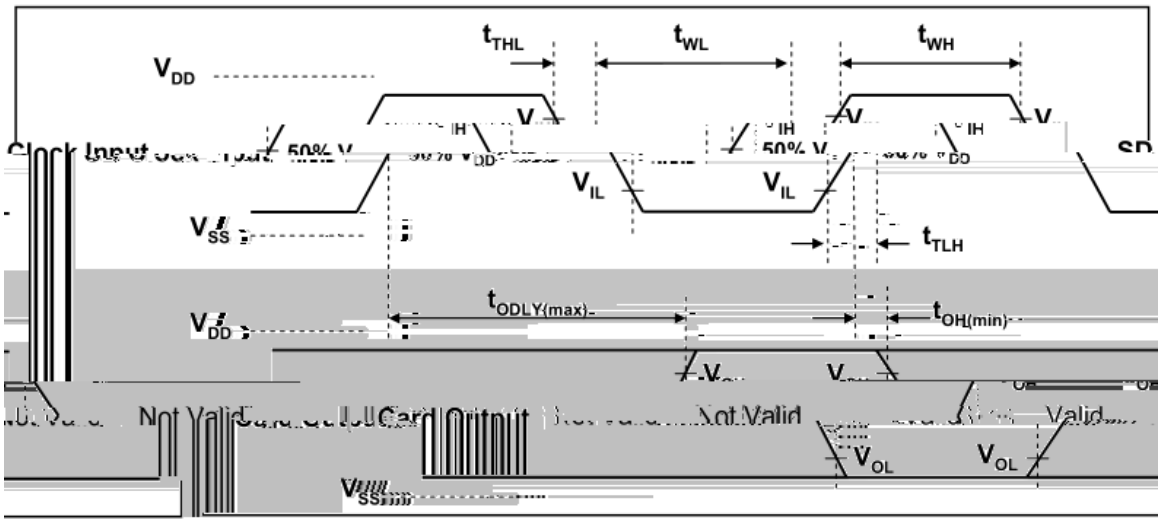


Card State Diagram (SPI mode)

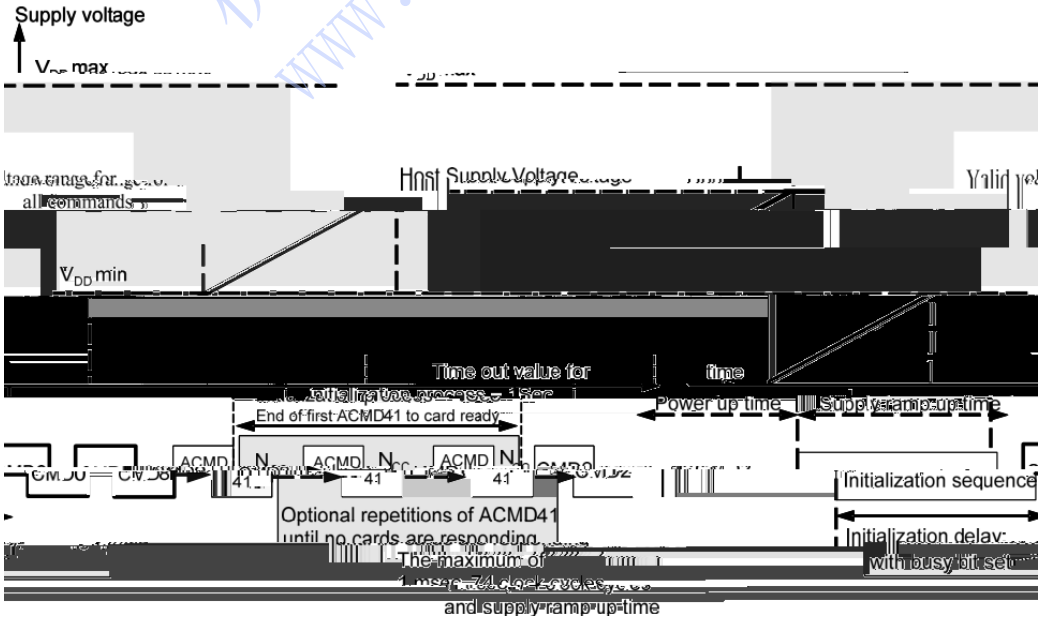
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Card Output Timing (High Speed Mode)



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'Power up time' is defined as voltage rising time from 0 volt to Vop min (refer to 6.6) and depends on application parameters such as the maximum number of SD Cards, the bus length and the characteristic of the power supply unit.