SD NAND SD 2

SD SEND OP COND (ACMD41) OCR HCS

(ACMD41)

ACMD41 :

(1) (23-0) “ CMD41 ”

OCR ACMD41 (31-24)

(2) (23-0) “ ACMD41 ”

(31-24)

(3) ACMD41 ACMD41 1

2

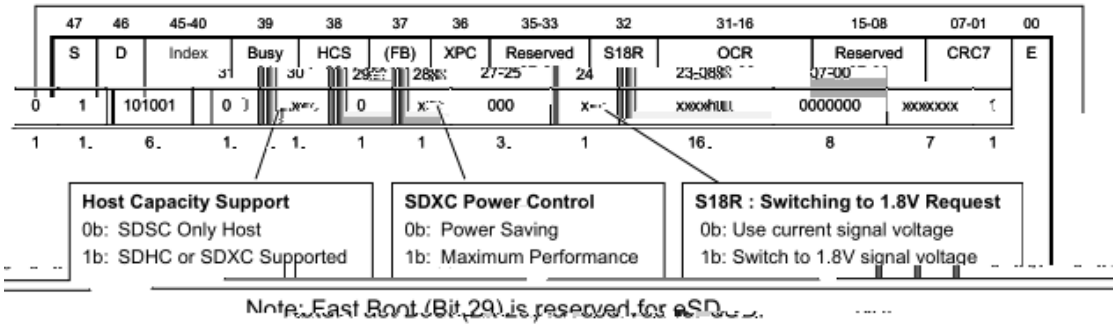
ACMD41 SDXC XPC-0 “ Default Speed ”

“ SDR12 ” 100mA “ Speed Class ” “ SD Status ” “ Class O ”

SDXC XPC-1 Default Speed SDR12 150mA

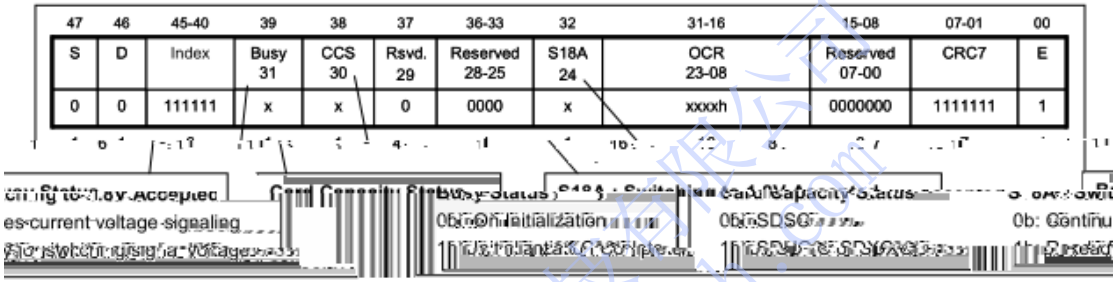
”

(1) Argument of ACMD41



Argument of ACMD41

(2) Response of ACMD41 (R3)



Response of ACMD41

Busy (Bit 31) 1 ccs (Bit 30) S18A (Bit 24)

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