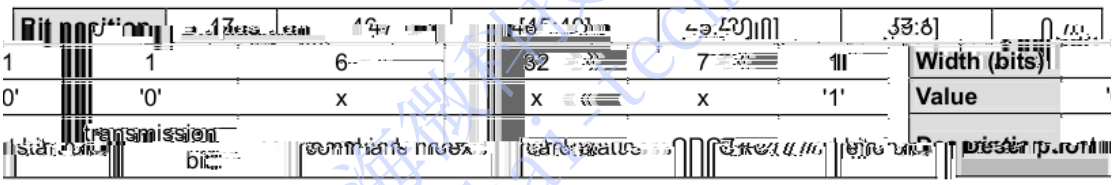
CMD

(0) (card = 0) x ″
R3 ( ) CRC
(1)

SD SDIO R4 R5 SDIOI
SDIO :

( ): 48 45:40
(0 63) 32



Response R1

R1b R1

136 CID CMD2 CMD10
CMD9 [127 ... 1]CID CSD 1]
[0]

Bit position	135	134	[133:128]	[127:1]	0
Width (bits)	1	1	6	127	1
Value	'0'	'0'	'111111'	x	'1'
Description	start bit	transmission bit	reserved	CID or CSD register incl. internal CRC7	end bit

Response R2

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48 OCR ACMD41

48 45:40 – – '000011'(
5 scmd3) 16 MSB RCA

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